## Formal Drawings MARK OFFICE PARSO PLANSO P

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No	9/561,794
Priority Filing Date Ma	ay 1, 2000
Inventor Alan R.	. Reinberg
Assignee Micron Technology	ology, Inc.
Priority Group Art Unit	2811
Priority Examiner	C. Nguyen
Attorney's Docket No	MI22-1704
Title: Microelectronic Device Fabricating Method, Integrated Circuit, and	
Intermediate Construction	

Assistant Commissioner for Patents Washington, D. C. 20231
Attention: Official Draftsman

## **LETTER SUBMITTING FORMAL DRAWINGS**

Please enter the enclosed formal drawings in the above-referenced application in place of the originally filed drawings.

Acknowledgment of receipt of the formal drawing sheets and their acceptance into the file is requested.

Respectfully submitted,

Date: 08 May 2001

By:

James H. Lake

Reg. No.: 44,854

WELLS, ST. JOHN, ROBERTS, GREGORY & MATKIN P.S.

601 W. First Avenue, Suite 1300

Spokane, WA 99201-3828

(509) 624-4276

Enclosures: Nine (9) sheets of drawings (Fig. 1A-20B)

EL465782877